

## Phase-Locked Loop Synthesizer Simulation (McGraw-Hill Electronic Engineering)

Giovanni Bianchi



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**Phase-Locked Loop Synthesizer Simulation (McGraw-Hill Electronic Engineering)** Giovanni Bianchi Phase locked loops (PLLs) are electronic circuits that ensure that a communications signal stays locked on a given frequency. Their design is crucial to the workings of wireless communications systems. Virtually all transceivers use PLLs to synthesize the stable, high frequency oscillations necessary for radio & wireless. This book describes how to calculate PLL performances by using standard mathematical or circuit analysis programs. Theoretical descriptions are limited to the minimum needed to explain how to perform calculations. Although presented methods of analysis can be implemented with many commercial programs, their description always refers to Mathcad and SIMetrix.

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